

Subst. Form PTO-1449 APPLICANT'S(S) INFORMATION DISCLOSURE STATEMENT	Atty. Docket No.: HIT 2 010-1-1	Serial No.: UNKNOWN 09/240,975
	Applicant(s): Naoki Mitsuishi et al.	
	Filing Date: Herewith 1/29/99	Group: 2751

U.S. PATENT DOCUMENTS

Initial*		Document No.	Date	Name	Class	Subcl.	Filing Date
RGB	AA	4,128,900	12/1978	Lappington	365	94	
RGB	AB	4,211,919	7/1981	Ugon	235	487	
RGB	AC	4,279,024	7/1981	Schrenk	365	185.22	
RGB	AD	4,366,540	12/1982	Berglund et al.	713	501	
RGB	AE	4,377,857	3/1983	Tickle	365	185.33	
RGB	AF	4,382,279	5/1983	Ugon	712	37	
RGB	AG	4,520,458	5/1985	Hattori et al.	711	167	
RGB	AH	4,575,621	3/1986	Dreifus	235	380	
RGB	AI	4,605,844	8/1986	Haggan	235	380	
RGB	AJ	4,663,741	5/1987	Reinschmidt et al.	365	155	
RGB	AK	4,684,791	8/1987	Bito	235	380	
RGB	AL	4,698,750	10/1987	Wilkie et al.	365	185.04	
RGB	AM	4,701,886	10/1987	Sakakibara et al.	714	718	
RGB	AN	4,718,038	1/1988	Thaden	712	37	
RGB	AO	4,718,038	1/1988	Yoshida	711	164	
RGB	AP	4,734,568	3/1988	Watanabe	235	487	
RGB	AQ	4,749,982	6/1988	Rikuna et al.	340	146.2	
RGB	AR	4,752,871	6/1988	Sparks et al.	365	185.11	
RGB	AS	4,811,294	3/1989	Kobayashi et al.	365	185.22	

FOREIGN PATENT DOCUMENTS

		Document No.	Date	Country	Class	Subcl.	Translation?
RGB	AT	19665	2/1981	Japan	—	—	Yes
RGB	AU	2 079 996	1/1982	Great Britain	—	—	English text
RGB	AV	0 081 873	6/1983	Europe	—	—	No
RGB	AW	28 40 305	3/1980	Germany	—	—	Yes
RGB	AX	2 430 065	1/1980	France	—	—	No

OTHER ART

RGB	AY	"HITACHI MICROCOMPUTER DATA BOOK, 8-Bit Single Chip", pp. 825 and 838-842 (1984). 823-865 (1984).					
-----	----	---	--	--	--	--	--

RGB	AZ	"THE HITACHI HYORON", vol. 68, no. 7, pp. 29-32 (1986). (w/translation)
	2	
RGB	BA	Motorola Microcomputers, Series C, Austin, Texas, Motorola, 1984, pp. (3-827) - (3-850).
	3	
RGB	BB	L.L. Goss, "Single chip microcomputer with EEROM allows flexible system design", 1983, WESCOM 83, vol. 27, 34/2, pp. 1-4.
	4	
RGB	BC	F. Gruppuso, et al., "On-chip EEPROM holds changing register data for 8-bit microcomputer", December 1984, ELECTRONIC DESIGN, vol. 32, no. 25, pp. 201-209.
	5	
RGB	BD	Electronique Industrielles, "Programmeur de memoires PROM utilisant un système SBC 80," October 1979, no. 274, pp. 61-63. (w/translation)
	6	
RGB	BE	Wescon Technical Papers, "High Density EPROMs," 1984, vol. 29, no. 1, pp. 1-9.
	7	
RGB	BF	"Electrically erasable memories apply for system work", ELECTRONIC DESIGN, October 13, 1983, pp. 145-148 and 150.
	8	
RGB	BG	John Uffenbeck, "Microcomputers and Microprocessors: The 8080, 8085, and L-80 Programming Interfacing and Troubleshooting", 1985, pp. 194-195.
	9	
RGB	BH	"Single-Chip Microcomputer", Motorola, Inc., 1984, pp. (3-1033) - (3-1058).
	10	
RGB	BI	Electronics Week, "Memories, One-Megabit EPROMs Invade Disk Territory", April 22, 1985, pp. 52-54.
	11	
RGB	BJ	Nikkei Electronics, "Large Capacity EPROM - Its Various Direction Great Change Since 1M", August 12, 1985, pp. 119-137.
	12	
RGB	BK	Andrew M. Veronis, "Memory", in Andrew M. Veronis, Microprocessing Hardware and Applications (Reston Virginia, Prentice-Hall Company (1984), pp. 82.
	13	
RGB	BK	French Search Report dated July 17, 1992; FR 8703258, FA 392294.
	14	
Examiner: <u>Reginald M. Bragdon</u> <u>Reginald M. Bragdon</u>		Date Considered: <u>5-17-2000</u> <u>6-14-2001</u>
* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if in conformance and not considered. Include copy of this form with next communication to applicant.		